

Title (en)

Timeout process circuit and receiver including this timeout process circuit.

Title (de)

Schaltung zur Zeitüberschreitungsverarbeitung und Empfänger mit dieser Schaltung.

Title (fr)

Circuit de traitement de dépassement du temps et récepteur comportant ce circuit.

Publication

EP 0665668 A3 19960228 (EN)

Application

EP 95101156 A 19950127

Priority

- JP 767194 A 19940127
- JP 29724194 A 19941130

Abstract (en)

[origin: EP0665668A2] A timeout process circuit for performing a timeout detection process incorporated in a receiver, having a timer for incrementing time data indicating current time, a memory including data table to store a reception time of the cell which being most recently received by the receiver, a register for storing a timeout value indicating a maximum permitted time interval of the cell reception, a controller for reading out the reception time stored in the data table when the controller receiving the timeout detection start signal from the timer, an adder for adding the reception time from the data table and the timeout value stored in the first register means, a comparator for comparing the result of the addition by the adder with the time data from the timer, and a decision circuit for receiving the comparison result from the comparator and deciding whether or not the cell of the frame in the reassembly is a timeout based on the comparison result. In the timeout process circuit, the reception time of the cell is stored in the data table when the cell is received by the receiver. <IMAGE>

IPC 1-7

H04L 12/40

IPC 8 full level

G06F 11/30 (2006.01); **G06F 13/00** (2006.01); **H04Q 11/04** (2006.01)

CPC (source: EP KR US)

G06F 13/00 (2013.01 - KR); **H04Q 11/0478** (2013.01 - EP US); **H04L 2012/565** (2013.01 - EP US); **H04L 2012/5652** (2013.01 - EP US)

Citation (search report)

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- [A] GUILLEMIN F ET AL: "The spacer-controller: Architecture and first assessments", BROADBAND COMMUNICATIONS. IFIP TC6 WORKSHOP, ESTORIL, PORTUGAL, 20-22 JAN. 1992, vol. C-4, ISSN 0926-549X, IFIP TRANSACTIONS C (COMMUNICATION SYSTEMS), 1992, NETHERLANDS, pages 313 - 323, XP000430627

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EP0907298A1; US6411622B1

Designated contracting state (EPC)

DE FR GB

DOCDB simple family (publication)

EP 0665668 A2 19950802; EP 0665668 A3 19960228; EP 0665668 B1 20020828; CN 1098582 C 20030108; CN 1119760 A 19960403; DE 69527862 D1 20021002; DE 69527862 T2 20030327; JP H07253936 A 19951003; KR 0147509 B1 19980915; KR 950024077 A 19950821; TW 340923 B 19980921; US 5706425 A 19980106

DOCDB simple family (application)

EP 95101156 A 19950127; CN 95101677 A 19950127; DE 69527862 T 19950127; JP 29724194 A 19941130; KR 19950001508 A 19950127; TW 84100997 A 19950207; US 37823495 A 19950126