

Title (en)  
METHOD FOR CONTROLLING A SPRYTE RENDERING PROCESSOR.

Title (de)  
VERFAHREN ZUM REGELN EINES PROZESSORS ZUR GRAPHIKDARSTELLUNG.

Title (fr)  
PROCEDE DE COMMANDE D'UN PROCESSEUR GENERATEUR DE LUTINS.

Publication  
**EP 0667017 A4 19960103 (EN)**

Application  
**EP 92925041 A 19921102**

Priority  
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Abstract (en)  
[origin: WO9410642A1] Linked lists of spryte control blocks are prepared in memory (108) and transversed by a spryte rendering engine. Each spryte control block (106) controls the rendering of a respective spryte into the display buffer, and contains such information as a pointer to source data for the corresponding spryte, positional an incrementing specifications for a destination quadrilateral, a control word for manipulations to be performed on the spryte image source data, and an indication of which of several available formats the spryte image source data is packed in. Once the linked list is prepared, the spryte rendering engine (106) can be called by writing certain values into specific memory-mapped hardware registers, and then writing dummy data to an address recognized by the hardware as a command to initiate the spryte rendering operation (106).

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IPC 8 full level  
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CPC (source: EP)  
**G06T 15/80** (2013.01)

Citation (search report)

- [X] WO 9015396 A2 19901213 - ATARI CORP [US]
- [X] WO 9015395 A1 19901213 - ATARI CORP [US]
- See references of WO 9410642A1

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**US 9209350 W 19921102**; AU 3124493 A 19921102; BR 9207171 A 19921102; EP 92925041 A 19921102; JP 51100892 A 19921102