

Title (en)  
Multi-processor module.

Title (de)  
Multiprozessormodul.

Title (fr)  
Module multi-processeur.

Publication  
**EP 0668204 A1 19950823 (EN)**

Application  
**EP 95300671 A 19950202**

Priority  
GB 9403044 A 19940217

Abstract (en)  
A multi-processor module (33) for use in a solid-state interlocking system for a railway network includes a first processor for receiving instructions concerning the operation of parts of the trackside equipment (40) within the network and carrying out those instructions in dependence upon the current overall status of that trackside equipment, and a second processor for receiving the instructions from the first processor and effecting the operation of the appropriate trackside equipment via a data link. The first processor runs at a higher clock speed than the second. A program run in the first processor is arranged to be slowed down in those areas which interface with the second processor. The slowing down may be effected by the inclusion of delay loops, no-operation instructions, or by the increasing of time constants in timing hardware. <IMAGE>

IPC 1-7  
**B61L 19/06**; **B61L 27/00**

IPC 8 full level  
**B61L 19/06** (2006.01); **B61L 27/00** (2006.01)

CPC (source: EP)  
**B61L 19/06** (2013.01)

Citation (search report)  
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