

Title (en)

PROGRAMMABLE LOGIC DEVICES AND CONFIGURABLE LOGIC NETWORKS.

Title (de)

PROGRAMMIERBARE LOGISCHE VORRICHTUNGEN UND KONFIGURIERBARE LOGISCHE NETZWERKE.

Title (fr)

DISPOSITIFS LOGIQUES PROGRAMMABLES ET RESEAUX LOGIQUES CONFIGURABLES.

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Application

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Abstract (en)

[origin: WO9411950A1] A programmable logic device (PLD) and configurable logic network in which one or more logic combination networks (LCN) each receives logic inputs from two or more PLDs (PLD1, PLD2) and generates logic outputs (O, P) which can then be used to provide inputs to programmable logic networks (POR, UCL,...) for implementing logic functions of various types and functionality. Each programmable logic device includes an AND logic array (FAND...) having inputs for receiving signals (Ax, Bx) and generating product term output signals and an OR logic array (OG...) having inputs for receiving signals and generating sum term output signals (OF...). One or both of the AND logic and OR logic arrays is programmable and the logic arrays are interconnected to apply output signals from one of them as input signals to the other one, the output from which provides PLD output signals. The logic combination networks may be fixed logic networks (LCN100) or programmable logic function generators (UBLFG20, UBFF2P) that produce outputs controlled by a set of programmable inputs (CNx, DNx) to the generator as a function of the logic inputs (O, P) received from the programmable logic devices.

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