

Title (en)

METHOD AND APPARATUS FOR SOLDERING CIRCUIT BOARDS.

Title (de)

VERFAHREN UND VORRICHTUNG ZUM LÖTEN VON LEITERPLATTEN.

Title (fr)

PROCEDE ET APPAREIL DE BRASAGE DE CARTES DE CIRCUITS.

Publication

EP 0669857 A4 19960522 (EN)

Application

EP 94900343 A 19930920

Priority

US 9308950 W 19930920

Abstract (en)

[origin: WO9508403A1] A method and apparatus for depositing solder on the terminal pads (10) of printed circuit boards (12) in which a solder resist layer (16) or layers (16, 17) having a thickness corresponding to the desired solder height border the pads. Molten solder from a reservoir (58) is directed by nozzles (72) against the sides of the board (12) to fill the cavities extending above the terminal pads while the board (12) is moving via a conveyor mechanism relative to the reservoir (58). The cavities when filled with molten solder are covered by a suitable element such as a flexible belt (52) or roller. The molten solder within the covered cavities is then cooled below its solidification point and the covering element removed. If desired, part or all of the solder resist layer (16) or layers (16, 17) may then be stripped from the board (12) to leave solder pads extending above the surface of the board (12).

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Citation (search report)

- [A] US 4493857 A 19850115 - KNIGGE ROLF [DE], et al
- [A] PATENT ABSTRACTS OF JAPAN vol. 014, no. 028 (E - 875) 19 January 1990 (1990-01-19)
- See references of WO 9508403A1

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