

Title (en)

Circuit to control the voltage between well and source of MOS logic circuit transistors and servo system of the power supply

Title (de)

Schaltung zur Steuerung von Spannungen zwischen Wanne und Source von Transistoren einer logischen MOS-Schaltung und Servosystem zu seiner Leistungsverorgung

Title (fr)

Circuit pour commander les tensions entre caisson et sources des transistors d'un circuit logique MOS et système d'asservissement de son alimentation

Publication

EP 0674252 B1 19990804 (FR)

Application

EP 95400649 A 19950323

Priority

FR 9403641 A 19940325

Abstract (en)

[origin: EP0674252A1] A current source (25) connected between the supply voltage (27) and the drain of a reference MOS transistor (24) imposes a reference current I(ref) on the transistor drain. The drain and grid of the transistor are maintained at the same voltage by a short circuit (CC). A comparator (21) fed from the supply voltage (V+) (27) compares the drain voltage of the transistor (24) with a reference voltage V(tnref) and the difference is fed to a voltage controlled oscillator (22). The oscillator output supplies a multiplier (23) which feeds back to the container thus controlling the threshold voltages of all the transistors in the container.

IPC 1-7

G05F 3/24

IPC 8 full level

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EP 0674252 A1 19950927; **EP 0674252 B1 19990804**; CA 2145358 A1 19950926; CA 2145358 C 20030603; DE 69511138 D1 19990909; DE 69511138 T2 20000302; FR 2717918 A1 19950929; FR 2717918 B1 19960524; JP H0897374 A 19960412; US 5682118 A 19971028

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