

Title (en)

Circuit for operating low-pressure discharge lamps

Title (de)

Schaltungsanordnung zum Betrieb von Niederdruckentladungslampen

Title (fr)

Circuit pour alimenter des lampes à décharge basse-pression

Publication

**EP 0679046 B1 19990602 (DE)**

Application

**EP 95103597 A 19950313**

Priority

DE 4410492 A 19940325

Abstract (en)

[origin: JPH07272885A] PURPOSE: To guarantee elimination of a sinusoidal system current as much as possible, to provide an improved system power factor compared with the conventional technology, and to make suitable to light a low pressure discharge lamp having a relatively high lighting voltage. CONSTITUTION: This circuit device interrupts the charging of a smoothing capacitor C2 to feed the power to an inverter WR by the switching rhythm of the inverter WR. And thereby, a system having the system power factor more than 0.98, and capable of removing a nearly sinusoidal wave form current is provided in cooperation with a storage reactor L1 preconnected to high-frequency wave rectifying bridges D1, D2, D3, and D4, and a system voltage rectifier output capacitor C1, and interacting with a negative feedback capacitor CG and a backup capacitor CS.

IPC 1-7

**H05B 41/29**

IPC 8 full level

**H05B 41/24** (2006.01); **H05B 41/28** (2006.01); **H05B 41/282** (2006.01)

CPC (source: EP US)

**H05B 41/28** (2013.01 - EP US); **H05B 45/355** (2020.01 - EP); **Y10S 315/07** (2013.01 - EP)

Cited by

US5757143A; CN1107439C; US8067902B2; WO9719578A1; WO2010027392A1

Designated contracting state (EPC)

BE DE FR GB IT NL

DOCDB simple family (publication)

**DE 4410492 A1 19950928**; DE 59506071 D1 19990708; EP 0679046 A1 19951025; EP 0679046 B1 19990602; JP 3599823 B2 20041208; JP H07272885 A 19951020; US 5521467 A 19960528

DOCDB simple family (application)

**DE 4410492 A 19940325**; DE 59506071 T 19950313; EP 95103597 A 19950313; JP 8883095 A 19950322; US 38917995 A 19950215