Title (en)

A damper circuit for a device for indicating the level of a liquid in a tank, particularly for motor vehicles

Title (de)

Dämpfungsschaltung für eine Einrichtung zur Anzeige des Niveaus einer Flüssigkeit in einem Tank, insbesondere für Kraftfahrzeuge

Title (fr)

Circuit amortisseur pour un dispositif d'indication du niveau de liquide dans un réservoir, en particulier pour des véhicules à moteur

Publication

EP 0683383 B1 19990804 (EN)

Application

EP 95107112 A 19950511

Priority

IT TO940396 A 19940517

Abstract (en)

[origin: EP0683383A1] A damper circuit (CS) for a device (I) for indicating the level of a liquid in a tank comprises an indicator instrument (S) connected to the output of an electric level sensor (T) in the form of a resistor (Rx) the resistance of which is variable in dependence on the level of the liquid. A first capacitor (C2) is connected by means of a first resistor (R1) to the input (Nm) of the instrument (S); a first operational amplifier (OP1) has its inverting input and its output connected to the input (Nm) of the instrument (S) by means of a second resistor (R2) and a third resistor (Rs) respectively and the non-inverting input connected to the first capacitor (C2) which is connected between the non-inverting input of the first operational amplifier (OP1) and ground. A second capacitor (C1) is connected between the first capacitor (C2) and a DC voltage (Vbat); the terminals of the third resistor (Rs) are connected to the inputs of a second operational amplifier (OP2) the output of which is connected to the non-inverting input of the first amplifier (OP1) by means of a fourth resistor (Rw). A field-effect transistor (Ft1) is connected between the input of the instrument (S) and the level sensor (T); a fifth resistor (RL) is connected between a voltage (V+) and the control electrode of the field-effect transistor (Ft1), transistors (TR1, TR2) have their emitter-base junctions connected in parallel to the third resistor (Rs) and the irrespective collectors connected, respectively, to the control electrode of the field effect transistor (Ft1) and to ground. <IMAGE>

IPC 1-7

G01F 23/00; G01F 23/30

IPC 8 full level

G01F 23/00 (2006.01); **G01F 23/30** (2006.01)

CPC (source: EP)

G01F 23/303 (2013.01); **G01F 23/806** (2022.01)

Cited by

CN110160604A; US11181410B2; EP0829965A1; US5912617A

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EP 0683383 A1 19951122; **EP 0683383 B1 19990804**; DE 69511165 D1 19990909; DE 69511165 T2 19991125; IT 1273206 B 19970707; IT TO940396 A0 19940517; IT TO940396 A1 19951117

DOCDB simple family (application)

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