

Title (en)
Inductor chip device.

Title (de)
Chip-induktivität Anordnung.

Title (fr)
Dispositif à inductance de puce.

Publication
EP 0685857 A1 19951206 (EN)

Application
EP 95303410 A 19950522

Priority
GB 9411107 A 19940603

Abstract (en)
An inductor chip device for mounting on multichip-module, direct-chip-attach or surface-mount assemblies comprises a dielectric substrate (12), a spiral metallisation structure (14) defined on one major surface of the substrate, and a plurality of solder bumps (18, 19) defined on the other major surface of the substrate, the spiral structure being electrically connected to the plurality of solder bumps by means of metal-filled vias. Single or multilayer inductors may be defined, the latter providing higher inductance values in the same chip area. The multilayer inductors are constructed using a multilayer metal-polyimide or similar dielectric structure. The chip inductor provides a very small, accurate, discrete device with high inductance, high Q-factor and high self-resonant frequency. <IMAGE>

IPC 1-7
H01F 17/00; **H01F 27/29**

IPC 8 full level
H01F 41/04 (2006.01); **H01F 17/00** (2006.01); **H01F 27/29** (2006.01)

CPC (source: EP)
H01F 17/0006 (2013.01); **H01F 27/292** (2013.01)

Citation (applicant)
GB 2226707 A 19900704 - MICRO STRATES INC [US]

Citation (search report)
• [A] DE 3927181 A1 19900301 - MURATA MANUFACTURING CO [JP]
• [A] EP 0398485 A1 19901122 - PLESSEY OVERSEAS [GB]
• [DA] GB 2226707 A 19900704 - MICRO STRATES INC [US]
• [A] EP 0497556 A1 19920805 - SHARP KK [JP]
• [A] PATENT ABSTRACTS OF JAPAN vol. 11, no. 182 (E - 515) 11 June 1987 (1987-06-11)

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NL1036092C; EP1302954A1; FR2830683A1; DE10144464A1; DE10144464C2; US11342896B2; US11367555B2; US6621378B2; US11139582B2; WO9933108A1; US11908617B2; US8053890B2; US11264167B2; US11270843B2; US11373908B2; US11677373B2; US11929199B2; US11161773B2; US11594457B2; US11962057B2; WO2021211855A1; US11367939B2; US11894594B2

Designated contracting state (EPC)
AT DE ES FR IT

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