

Title (en)

A device for selecting design options in an integrated circuit

Title (de)

Schaltung zur Auswahl von Entwurfsmöglichkeiten in einer integrierten Schaltung

Title (fr)

Circuit pour la sélection d'options de conception dans un circuit intégré

Publication

EP 0685887 B1 19980701 (EN)

Application

EP 94830261 A 19940530

Priority

EP 94830261 A 19940530

Abstract (en)

[origin: EP0685887A1] The device described permits selection between two design options of an integrated circuit (10) by causing a corresponding circuit unit (12) of the integrated circuit to adopt one of two possible different operative states. It comprises an inverter (16), of which the output terminal (17) is connected to the control terminal (13) of the circuit unit (12) and the input terminal (18) is connected to a first supply terminal (Vdd) by means of a conductor (A, B) which can be broken by means outside the integrated circuit, and to the second supply terminal by means of a capacitor (C1) in parallel with a diode (D1) connected for reverse conduction. The device described does not require control signals, takes up a very small area, has practically zero consumption, and can be formed in unlimited numbers on the same integrated circuit. <IMAGE>

IPC 1-7

H01L 23/525

IPC 8 full level

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CPC (source: EP)

G06F 11/006 (2013.01); **G11C 7/1045** (2013.01); **H01L 27/0788** (2013.01); **H03K 19/1731** (2013.01); **H03K 19/1735** (2013.01)

Cited by

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