

Title (en)

DMD Architecture and timing for use in a pulse-width modulated display system

Title (de)

Struktur von DMDs und Ablaufsteuerung zur Anwendung in einem Anzeigesystem mit Pulsbreitenmodulation

Title (fr)

Architecture et temporisation de DMD pour l'utilisation dans un système d'affichage à modulation de largeur d'impulsions

Publication

EP 0689345 A2 19951227 (EN)

Application

EP 95114139 A 19920401

Priority

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- US 67876191 A 19910401

Abstract (en)

An integrated circuit device comprises an array of spatial light modulating elements divided into subarrays by control circuitry, such that each said subarray operates as an independent array of spatial light modulating elements. An array of decoders is provided such that each subarray of said spatial light modulating elements receives control signals through one of said decoders, wherein at least one said decoder delivers signals to one unique subarray. Additionally, an array of input registers is provided such that data in said registers is transferred to said subarrays in response to signals from said decoders, wherein at least one said register transfers data to a unique subarray.

IPC 1-7

H04N 3/12; **G09G 3/34**

IPC 8 full level

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CPC (source: EP KR US)

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EP 0507270 A1 19921007; **EP 0507270 B1 19970723**; CA 2063744 A1 19921002; CA 2063744 C 20021008; CN 1040493 C 19981028; CN 1064804 C 20010418; CN 1064805 C 20010418; CN 1068232 A 19930120; CN 1115935 A 19960131; CN 1115936 A 19960131; DE 69220998 D1 19970904; DE 69220998 T2 19980122; DE 69232676 D1 20020814; DE 69232676 T2 20030206; EP 0689343 A2 19951227; EP 0689343 A3 19960515; EP 0689344 A2 19951227; EP 0689344 A3 19960515; EP 0689345 A2 19951227; EP 0689345 A3 19960515; EP 0689345 B1 20020710; JP H05183851 A 19930723; KR 100253106 B1 20000501; KR 920020249 A 19921120; TW 226513 B 19940711; US 5278652 A 19940111; US 5339116 A 19940816; US 5523803 A 19960604; US 5745193 A 19980428

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