

Title (en)

Data processing system having a high speed burst access

Title (de)

Datenverarbeitungssystem mit Hochgeschwindigkeit-Stossbetriebzugriff

Title (fr)

Système de traitement de données avec accès en rafale à grande vitesse

Publication

EP 0701216 A1 19960313 (EN)

Application

EP 95114246 A 19950911

Priority

JP 24084794 A 19940909

Abstract (en)

A data processing system includes a microprocessor and a memory coupled to each other, and has a burst access function in which when an active burst request signal is supplied to the memory, only a first address is outputted to the memory, so that a plurality of items of data are accessed sequentially from the first address. A memory region of the memory is divided into a plurality of memory blocks, and the microcomputer includes a register for storing a burst access/single access information for each of the memory blocks, and a decoder receiving an address to be outputted to the memory, for knowing to which memory block a memory region to be accessed belongs. When the memory region to be accessed is a memory block for the burst access, a burst request signal is activated. <MATH>

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G06F 13/42; **G06F 13/28**

IPC 8 full level

G06F 13/16 (2006.01); **G06F 12/02** (2006.01); **G06F 12/06** (2006.01); **G06F 13/28** (2006.01); **G06F 13/32** (2006.01); **G06F 13/42** (2006.01)

CPC (source: EP KR US)

G06F 13/16 (2013.01 - KR); **G06F 13/28** (2013.01 - EP US); **G06F 13/4243** (2013.01 - EP US)

Citation (search report)

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EP 0701216 A1 19960313; **EP 0701216 B1 20020123**; DE 69525116 D1 20020314; DE 69525116 T2 20021002; JP 3153078 B2 20010403; JP H0883237 A 19960326; KR 100225727 B1 19991015; KR 960011677 A 19960420; US 5826108 A 19981020

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