

Title (en)
FLEXCELL GATE ARRAY

Title (de)
FLEXCELL-GATE-ARRAY

Title (fr)
MATRICE PREDIFFUSEE A CELLULES SOUPLES

Publication
EP 0702849 A1 19960327 (EN)

Application
EP 94919331 A 19940603

Priority
• US 9406214 W 19940603
• US 7400293 A 19930607

Abstract (en)
[origin: WO9429902A1] Novel gate arrays incorporate simple, but ingenious architectural features. A programmable layer of polycrystalline silicon is utilized to improve logic and RAM densities. The programmable layer of polycrystalline silicon is capable of providing electrical connections within a particular logic cell, without requiring the use of electrical interconnections within a dedicated routing channel. This allows increased density of the gate array. By providing programmable polycrystalline silicon, polycrystalline silicon contact connections are placed only where needed, avoiding the need to leave space between adjacent polycrystalline silicon lines for all potential contact connections.

IPC 1-7
H01L 27/118

IPC 8 full level
H01L 21/82 (2006.01); **H01L 27/118** (2006.01)

CPC (source: EP KR)
H01L 27/118 (2013.01 - EP KR); **H01L 27/11898** (2013.01 - EP)

Citation (search report)
See references of WO 9429902A1

Designated contracting state (EPC)
DE FR GB

DOCDB simple family (publication)
WO 9429902 A1 19941222; EP 0702849 A1 19960327; JP H09507000 A 19970708; KR 960702947 A 19960523

DOCDB simple family (application)
US 9406214 W 19940603; EP 94919331 A 19940603; JP 50193894 A 19940603; KR 19950705545 A 19951207