

Title (en)  
Multiplication circuit

Title (de)  
Multiplikationsschaltung

Title (fr)  
Circuit de multiplication

Publication  
**EP 0707274 B1 20000503 (EN)**

Application  
**EP 95114823 A 19950920**

Priority  
JP 26112294 A 19940930

Abstract (en)  
[origin: EP0707274A1] A multiplication circuit comprising a plurality of weighting circuit serially connected which comprises a plurality of switching means to which a common analog input voltage is inputted, the switching means being controlled by digital signals, a capacitive coupling with a plurality of capacitances each of which is connected to one of the switching means, an amplifier with high open gain to which an output of the amplifier is connected and a feedback capacitance connecting an output of the amplifier to an input. <IMAGE>

IPC 1-7  
**G06J 1/00**

IPC 8 full level  
**G06G 7/16** (2006.01); **G06J 1/00** (2006.01)

CPC (source: EP)  
**G06J 1/00** (2013.01)

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DE FR GB

DOCDB simple family (publication)  
**EP 0707274 A1 19960417**; **EP 0707274 B1 20000503**; DE 69516624 D1 20000608; DE 69516624 T2 20000831; JP 3511320 B2 20040329;  
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