Title (en)

INTEGRATED CIRCUIT COMPRISING AN OUTPUT STAGE WITH A MILLER CAPACITOR

Title (de)

INTEGRIERTE SCHALTUNG MIT EINER AUSGANGSSTUFE MIT EINEM MILLER-KONDENSATOR

Title (fr)

CIRCUIT INTEGRE COMPRENANT UN ETAGE DE SORTIE POURVU D'UN CONDENSATEUR DE MILLER

Publication

EP 0707758 A1 19960424 (EN)

Application

EP 95915980 A 19950505

Priority

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- EP 94201298 A 19940509
- IB 9500329 W 19950505

Abstract (en)

[origin: WO9531041A1] An integrated circuit, comprising an output stage with an input (I) which is coupled to a first and a second gate of an NMOS transistor (N1) and a PMOS transistor (P1), respectively, and an output which is connected to a first (Vdd) and a second supply terminal (Vss) via the PMOS transistor and the NMOS transistor, respectively. The output is coupled to the first gate via a series connection of a Miller capacitor (CMN) and switching means (N2, N3). The Miller capacitor limits the rate of increase of the voltage on the output, thus preventing interference. The switching means are rendered non-conductive ahead of the switching over from logic low to logic high. This prevents sudden discharging of the Miller capacitor which would otherwise cause interference itself.

IPC 1-7

H03K 19/003

IPC 8 full level

H03K 17/16 (2006.01); **H03K 17/687** (2006.01); **H03K 19/003** (2006.01); **H03K 19/0175** (2006.01); **H03K 19/0948** (2006.01)

CPC (source: EP US)

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