Title (en)

Circuit for detecting a fault state in a clock signal for microprocessor electronic devices

Title (de)

Schaltung zum Nachweis eines Fehlerzustandes eines Taktsignals für elektronische Mikroprozessorschaltungen

Title (fr)

Circuits pour détecter un état de défaut d'un signal d'horloge pour des dispositifs électroniques à microprocesseur

Publication

EP 0709775 B1 20020410 (EN)

Application

EP 94830506 A 19941027

Priority

EP 94830506 A 19941027

Abstract (en)

[origin: EP0709775A1] A circuit (2) for generation of a reset signal in an electronic device (1) of the type comprising a microprocessor (3) interlocked with a circuit generating a clock signal and memories of both the volatile type (4) and the non-volatile type (5) is capable of detecting a stop in the oscillation of said clock signal and generating a logical signal coupled with the reset input of the microprocessor (3). <IMAGE>

IPC 1-7

G06F 11/00; H03K 5/19

IPC 8 full level

G06F 11/30 (2006.01); **G06F 1/04** (2006.01); **G06F 11/00** (2006.01); **H03K 5/19** (2006.01)

CPC (source: EP US)

G06F 1/04 (2013.01 - EP US); **G06F 11/0757** (2013.01 - EP US); **H03K 5/19** (2013.01 - EP US)

Cited by

DE19644803A1; US5936452A; DE19644803C2; WO2005026959A3

Designated contracting state (EPC)

DE FR GB IT

DOCDB simple family (publication)

EP 0709775 A1 19960501; **EP 0709775 B1 20020410**; DE 69430372 D1 20020516; JP H0934737 A 19970207; US 5619643 A 19970408

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