

Title (en)

PHASE SHIFTING DEVICE

Title (de)

PHASENSCHIEBERANORDNUNG

Title (fr)

DISPOSITIF DE DECALAGE DE PHASE

Publication

**EP 0713259 A4 19961023 (EN)**

Application

**EP 95919652 A 19950529**

Priority

- JP 9501023 W 19950529
- JP 14383494 A 19940602
- JP 17597494 A 19940705

Abstract (en)

[origin: WO9534100A1] A circuit having nearly equally halves inputted electric power in such a way that the difference between the phases of the two outputs is 90 DEG . The outputs are fed to a first exciter which generates a circularly polarized wave. The wave is inputted into a second exciter, and the two outputs of which is combined in a synthesizing circuit. The opened ends of a shield case housing the first exciter and another shield case housing the second exciter are fitted together, and the first and second excitors, together with the cases, can be rotated relatively around the axis of the excitors. The phase difference between the input of the halving circuit and the output of the synthesizing circuit changes with the relative rotational angle between the first and second excitors.

IPC 1-7

**H01P 1/18; H01P 1/17**

IPC 8 full level

**H01P 1/165** (2006.01); **H01P 1/17** (2006.01); **H01P 1/18** (2006.01); **H01Q 3/32** (2006.01)

CPC (source: EP)

**H01P 1/165** (2013.01); **H01P 1/17** (2013.01); **H01P 1/18** (2013.01); **H01Q 3/32** (2013.01)

Citation (search report)

- [A] GB 2005083 A 19790411 - SECR DEFENCE
- [A] US 2530818 A 19501121 - GARDNER FOX ARTHUR
- [A] SOVIET INVENTIONS ILLUSTRATED Section EI Week 8711, 25 March 1987 Derwent World Patents Index; Class W02, AN 8707855911, XP002011626
- See references of WO 9534100A1

Cited by

US6470193B1; WO9847235A1

Designated contracting state (EPC)

DE FR GB

DOCDB simple family (publication)

**WO 9534100 A1 19951214**; DE 69526482 D1 20020529; DE 69526482 T2 20021024; EP 0713259 A1 19960522; EP 0713259 A4 19961023; EP 0713259 B1 20020424

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