

Title (en)
Pseudostereo processor for mobile receiver

Title (de)
Pseudostereoschaltung für mobilen Rundfunkempfänger

Title (fr)
Processeur de signaux pseudostéréo pour récepteur mobile

Publication
EP 0714222 A3 19981014 (DE)

Application
EP 95118291 A 19951121

Priority
DE 4442147 A 19941126

Abstract (en)
[origin: EP0714222A2] When a single-channel audio signal is present it is converted into a pseudo-stereo signal and distributed to two loudspeakers by a multiplexing unit. The quality of reception is evaluated by another circuit so that when it falls below a limit of acceptability the system is switched from stereo to mono operation in good time. The multiplex output from the FM tuner is applied to a stereo decoder which addresses a control unit causing the multiplexer to switch the loudspeaker inputs correctly according to the available signal in less than 100 ms.

IPC 1-7
H04S 5/00

IPC 8 full level
H04S 5/00 (2006.01)

CPC (source: EP)
H04S 5/00 (2013.01); H04S 3/002 (2013.01)

Citation (search report)
• [Y] EP 0503154 A2 19920916 - PIONEER ELECTRONIC CORP [JP]
• [YA] US 4833715 A 19890523 - SAKAI TSUNEO [JP]
• [A] EP 0412725 A2 19910213 - MATSUSHITA ELECTRIC IND CO LTD [JP]
• [A] EP 0142213 A1 19850522 - PHILIPS NV [NL]
• [Y] PATENT ABSTRACTS OF JAPAN vol. 12, no. 449 (E - 686) 25 November 1988 (1988-11-25)

Cited by
EP1021061A3; EP1024679A3

Designated contracting state (EPC)
AT CH DE FR GB IT LI NL

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EP 0714222 A2 19960529; EP 0714222 A3 19981014; EP 0714222 B1 20080903; AT E407541 T1 20080915; DE 4442147 A1 19960613;
DE 4442147 C2 20020214; DE 59511097 D1 20081016

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EP 95118291 A 19951121; AT 95118291 T 19951121; DE 4442147 A 19941126; DE 59511097 T 19951121