

Publication

EP 0714566 A4 19960717 (EN)

Application

EP 94922549 A 19940715

Priority

- US 9407894 W 19940715
- US 9127993 A 19930715

Abstract (en)

[origin: WO9502920A1] An electric power controller (8) for use with AC induction motors (9) that measures line voltage and current phase angles, and uses a microprocessor (10) to calculate delay values for firing silicon control rectifier (SCR) gates (76) to provide efficient amounts of current to the motors (9). The calculation is made using the measured phase angles, an inputted measured full load power factor for each motor, and corrected power factor value stored in a read-only-memory (ROM).

IPC 1-7

H02P 5/16

IPC 8 full level

H02J 3/18 (2006.01); **H02P 23/00** (2006.01); **H02P 25/04** (2006.01); **H02P 27/02** (2006.01)

CPC (source: EP)

H02J 3/1892 (2013.01); **H02P 23/26** (2016.02)

Citation (search report)

- [X] US 4459529 A 19840710 - JOHNSON LYNN S [US]
- [X] GB 2120422 A 19831130 - NAT RES DEV
- [X] WO 8402404 A1 19840621 - FAIRFORD ELECTRONICS LTD [GB]
- See references of WO 9502920A1

Designated contracting state (EPC)

DE GB IT NL

DOCDB simple family (publication)

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EP 0714566 A4 19960717; JP H09503375 A 19970331

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