

Title (en)

Voltage regulator for logical circuit in coupled mode

Title (de)

Spannungsregler für Logikschaltung in gekoppeltem Betriebszustand

Title (fr)

Régulateur de tension pour circuit logique en mode couple

Publication

EP 0715240 B1 20000607 (FR)

Application

EP 95410136 A 19951127

Priority

FR 9414604 A 19941130

Abstract (en)

[origin: EP0715240A1] The voltage regulator includes a first current source (11) which comprises two bipolar transistors connected between earth and a first resistance (R22) fed from a low-voltage DC supply (Vcc). It is controlled by the voltage developed across a second resistance (R21) by the current (I) drawn from the second source (14) in MOS technology. The second current source is connected in a current-mirror arrangement to a third current source (15) also in MOS technology whose drain electrode constitutes the output terminal (S) of the regulator. The regulated voltage (Vcs) applied to the load (Q) equals the product of the second source current and second resistance.

IPC 1-7

G05F 3/28; **G05F 3/26**

IPC 8 full level

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CPC (source: EP US)

G05F 3/267 (2013.01 - EP US); **Y10S 323/907** (2013.01 - EP)

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