

Title (en)

A semiconductor substrate, devices having the same and a method of manufacturing the same

Title (de)

Halbleiterwafer, Halbleiteranordnung enthaltende denselben und Verfahren zur dessen Herstellung

Title (fr)

Substrat semi-conducteur, dispositifs composés de ce dernier et procédé pour sa fabrication

Publication

EP 0724932 A1 19960807 (EN)

Application

EP 96300388 A 19960119

Priority

- JP 751595 A 19950120
- JP 8158995 A 19950406
- JP 8290595 A 19950407
- JP 31566195 A 19951204

Abstract (en)

A semiconductor substrate (1), devices comprising such a substrate, and a method of manufacturing them are disclosed. In particular, a method of processing the surface of a semiconductor substrate (1) comprises rotating the substrate (1), rotating a grinding wheel (6) about an axis (Z) which is substantially perpendicular to the axis of rotation of the substrate (X), and moving the axis of rotation of the wheel (Z) along an axis (Y) which is substantially perpendicular to the axis of rotation of the substrate (X) and the axis of rotation of the wheel (Z) to grind the substrate (1). A processed substrate comprises a process-transformed layer having an undulated surface. The thickness of the process transformed layer at its minimum is in the range of 0.1 μm to 0.5 μm and/or the difference between the minimum and maximum height of the undulated surface is in the range of 0.3 μm to 3 μm . <IMAGE>

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CPC (source: EP)

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Citation (search report)

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