

Title (en)  
Printed circuit board comprising a plurality of integrated circuits

Title (de)  
Platine mit mehreren integrierten Schaltungen

Title (fr)  
Platine de circuit imprimé comportant plusieurs circuits intégrés

Publication  
**EP 0730275 A3 19991110 (DE)**

Application  
**EP 96103201 A 19960301**

Priority  
DE 19507571 A 19950303

Abstract (en)  
[origin: EP0730275A2] The integrated circuits (2) operate with a supply voltage (V1) lower than the voltage (V2) of the external supply to the board (1) on which they are mounted; e.g. the new generation of CMOS circuits requires only 3.3 V instead of 5 V. To meet the requirement a supply voltage adaptor (3) is provided in the form of a regulator which produces the appropriate voltage drop and also suppresses fluctuations of output voltage. A simple diode with voltage drop in forward conduction may produce the necessary voltage difference.

IPC 1-7  
**G11C 5/00**; **G11C 5/14**

IPC 8 full level  
**H05K 1/18** (2006.01); **G11C 5/14** (2006.01); **H05K 1/02** (2006.01)

CPC (source: EP KR US)  
**G11C 5/147** (2013.01 - EP US); **H05K 1/0262** (2013.01 - EP US); **H05K 1/18** (2013.01 - KR); **H05K 1/18** (2013.01 - EP US); **H05K 2201/10174** (2013.01 - EP US); **H05K 2201/10196** (2013.01 - EP US); **H05K 2201/10689** (2013.01 - EP US)

Citation (search report)  
• [Y] GB 2258925 A 19930224 - SAMSUNG ELECTRONICS CO LTD [KR]  
• [Y] EP 0640974 A2 19950301 - FUJITSU LTD [JP]  
• [XY] ELECTRONICS INTERNATIONAL (8032), vol. 55, no. 3, 1982, New York, XP002115057

Designated contracting state (EPC)  
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