Title (en)

Reference current generator in CMOS technology

Title (de)

Referenzstromgenerator in CMOS-Technologie

Title (fr)

Générateur de courant de référence en technologie CMOS

Publication

EP 0733961 B1 20000705 (FR)

Application

EP 96400595 A 19960321

Priority

FR 9503352 A 19950322

Abstract (en)

[origin: EP0733961A1] In the current generator circuit a first current mirror (MP1,MP2) is formed with two circuit branches intended to be connected between two supply terminals (VDD,VSS). Ends of the branches comprise transistors (MP1,MN1;MP2,MN2) of opposite conductivity types connected in series. The second current mirror (MP1,MP3) generates an image (13) of the current (11) flowing in one of the branches. An active component (MN4) forming a variable conductance is mounted in series with the branch and is controlled in such a way that its value varies linearly with the current image (13). This conductance thus carries a current whose strength depends solely on the technological characteristics of the active component.

IPC 1-7

G05F 3/26; G05F 3/24

IPC 8 full level

G05F 3/24 (2006.01); **G05F 3/26** (2006.01)

CPC (source: EP US)

G05F 3/247 (2013.01 - EP US); **G05F 3/262** (2013.01 - EP US)

Cited by

EP1079294A1; EP0924590A1; US6353365B1; US7466202B2; WO2007118540A1

Designated contracting state (EPC)

CH DE FR GB LI

DOCDB simple family (publication)

EP 0733961 A1 19960925; **EP 0733961 B1 20000705**; DE 69609104 D1 20000810; DE 69609104 T2 20010315; FR 2732129 A1 19960927; FR 2732129 B1 19970620; US 5949278 A 19990907

DOCDB simple family (application)

EP 96400595 A 19960321; DE 69609104 T 19960321; FR 9503352 A 19950322; US 62041996 A 19960322