

Title (en)  
DATA PROCESSING DEVICE

Title (de)  
DATENVERARBEITUNGSVORRICHTUNG

Title (fr)  
DISPOSITIF DE TRAITEMENT DE DONNEES

Publication  
**EP 0734643 A4 19970402 (EN)**

Application  
**EP 94929254 A 19940920**

Priority  
• BR 9408102 A 19940920  
• US 9410593 W 19940920

Abstract (en)  
[origin: WO9609750A1] The invention relates to methods and structures for connecting circuits and circuit elements processing electrical signals having very fast transition times, including sub-nanosecond transition times. It includes a connecting structure (10) for connecting a multiplicity of signal lines (14A-14N) at a common point (12) to reduce unwanted reflections thereat; a single or multi-conductor female connector (30) for receiving thin elements such as ribbon cable ends, printed circuit (PC) board edges (32) and the like; a structure for connecting together a multiplicity of ribbon cables terminating in such multi-conductor female connectors, and a further structure (68, 70) for connecting together a multiplicity of such multi-cable connectors to obtain maximum packing thereof, and minimum conductor lengths thereto, to obtain processing rates up to twenty-fold over what is available with present connecting methods and structures.

IPC 1-7  
**H05K 7/20**

IPC 8 full level  
**G06F 13/40** (2006.01); **H01R 12/16** (2006.01); **H05K 7/02** (2006.01); **H05K 7/14** (2006.01); **H05K 7/20** (2006.01)

IPC 8 main group level  
**H01R** (2006.01)

CPC (source: EP)  
**G06F 13/409** (2013.01); **H05K 7/1442** (2013.01)

Citation (search report)  
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• [A] US 5119273 A 19920602 - CORDA ALBERT J [US]  
• [A] US 5346117 A 19940913 - KOHN HAROLD [US], et al  
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• [E] US 5548734 A 19960820 - KOLINSKI JERZY [US], et al  
• [T] SASAKI S ET AL: "HIGH-DENSITY, HIGH-PIN-COUNT FLEXIBLE SMD CONNECTOR FOR HIGH-SPEED DATA BUS", IEICE TRANSACTIONS ON ELECTRONICS, vol. E77-C, no. 10, 1 October 1994 (1994-10-01), pages 1694 - 1701, XP000496095  
• See references of WO 9609750A1

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