

Title (en)

Microprocessor systems for electronic postage arrangements

Title (de)

Mikroprozessorsysteme für elektronische Frankiermaschinenanordnungen

Title (fr)

Systèmes à microprocesseur pour arrangements de machines à affranchir électroniques

Publication

EP 0736846 A2 19961009 (EN)

Application

EP 96110413 A 19830125

Priority

- EP 92114140 A 19830125
- EP 86116058 A 19830125
- US 34387782 A 19820129

Abstract (en)

A microprocessor system suitable for an electronic postage arrangement comprises a microprocessor (10), register means (20, 21) coupled to said microprocessor for storing postage accounting data and a plurality of sensors (50, 51, 52) for detecting operation conditions in said postage meter. The microprocessor has a software routine stored in a permanent memory (11) to check said sensors periodically during operation and means responsive to the noncorrespondence of the outputs of said sensors and a unique code stored in said microprocessor permanent memory, upon a comparison performed in accordance with said software routine, for indicating an error condition. <IMAGE>

IPC 1-7

G07B 17/02

IPC 8 full level

G06F 12/16 (2006.01); **G07B 17/00** (2006.01); **G07B 17/04** (2006.01)

CPC (source: EP)

G07B 17/00362 (2013.01); **G07B 2017/00411** (2013.01)

Citation (applicant)

- US 4301507 A 19811117 - SODERBERG JOHN H, et al
- EP 0019515 A2 19801126 - FRIDEN CORP [US]
- US 3978457 A 19760831 - CHECK JR FRANK T, et al

Designated contracting state (EPC)

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DOCDB simple family (publication)

EP 0085385 A2 19830810; EP 0085385 A3 19841114; CA 1206619 A 19860624; DE 3382744 D1 19940519; DE 3382744 T2 19940901; DE 3382744 T3 20020905; DE 3382810 D1 19970213; DE 3382810 T2 19970522; DE 3382835 D1 20001130; DE 85385 T1 19851205; EP 0513880 A2 19921119; EP 0513880 A3 19930113; EP 0513880 B1 19970102; EP 0736846 A2 19961009; EP 0736846 A3 19961016; EP 0736846 B1 20001025; JP H0797417 B2 19951018; JP S58144989 A 19830829

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