

Title (en)  
BICMOS CURRENT MODE DRIVER AND RECEIVER

Title (de)  
BICMOS TREIBER UND EMPFÄNGER FÜR STROMBETRIEBSART

Title (fr)  
RECEPTEUR ET EXCITATEUR DE MODE COURANT BICMOS

Publication  
**EP 0739552 A1 19961030 (EN)**

Application  
**EP 94915426 A 19940428**

Priority  
• US 9404613 W 19940428  
• US 10477593 A 19930810

Abstract (en)  
[origin: WO9505033A1] An apparatus for reducing transmission delay times when transmitting differential signals in an integrated circuit along long interconnect lines (10, 11) includes a current mode line driver which converts the differential signal to be transmitted into a signal that has a relatively low peak-to-peak voltage and large differential current changes. A receiver responsive to differential current changes converts the signal back into an output differential signal having peak-to-peak voltages adaptable to subsequent logic stages. A feedback circuit (Q5, Q6) coupled to the interconnect lines (10, 11) and the receiver functions to clamp the interconnect lines (10, 11) to a predetermined voltage while allowing the output differential signal to have peak-to-peak voltages greater than the predetermined voltage.

IPC 1-7  
**H03K 19/018**; **H03K 19/0175**; **H03K 19/013**

IPC 8 full level  
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CPC (source: EP)  
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Citation (search report)  
See references of WO 9505033A1

Cited by  
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