Title (en)
  Signal processor for data conversion

Title (de)
  Signalprozessor für Datenumsetzung

Title (fr)
  Processeur à signaux à conversion de données

Publication
  EP 0742660 A1 19961113 (EN)

Application
  EP 96201266 A 19960508

Priority
  • NL 1000326 A 19950509
  • NL 1000682 A 19950628

Abstract (en)
  Signal processor for the processing of digital signals in the physical (for example, optical) domain. This is inter alia relevant for ATM systems in which control codes such as VPIs and VCIs at the inputs of sub-systems have to be changed. A symbol stream is lead through different delay branches. The number of delay branches is of such magnitude that at each moment, in at least one of the branches, both a "1" symbol and a "0" symbol is available. By the controlled opening and closing of the switches the symbol values can be changed. Moreover, no additional source of energy (light source) is necessary and the signal processor is completely transparent, that is to say, symbols at the output of the processor have exactly the same physical properties as those at the input of the processor. <IMAGE>

IPC 1-7
  H04L 12/54; H03M 7/00; G06E 1/02

IPC 8 full level
  G06E 1/02 (2006.01)

CPC (source: EP)
  G06E 1/02 (2013.01)

Citation (search report)
  • [XA] WO 9421088 A2 19940915 - BRITISH TELECOMM [GB], et al
  • [A] EP 0541855 A1 19930519 - SIEMENS AG [DE]

Cited by
  US6275311B1

Designated contracting state (EPC)
  AT BE CH DE DK ES FI FR GB GR IE IT LI LU NL PT SE

DOCDB simple family (publication)
  EP 0742660 A1 19961113; **EP 0742660 B1 20040804**; AT E272918 T1 20040815; DE 69633035 D1 20040909; DE 69633035 T2 20050818; NL 1000682 C2 19961112

DOCDB simple family (application)
  EP 96201266 A 19960508; AT 96201266 T 19960508; DE 69633035 T 19960508; NL 1000682 A 19950628