

Title (en)

DISASTER AVOIDANCE CLOCK FOR ANTI-LOCK BRAKING SYSTEM

Title (de)

SICHERHEITSTAKTGEBER FÜR SCHLUPFGEREDELTE BREMSANLAGEN

Title (fr)

HORLOGE DE SECURITE EN CAS DE DEFAILLANCE POUR SYSTEME DE FREINAGE ANTIBLOCAGE

Publication

**EP 0742919 A1 19961120 (EN)**

Application

**EP 94931824 A 19941012**

Priority

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Abstract (en)

[origin: WO9521412A1] On an integrated circuit (230), a back-up oscillator circuit (231) is provided which includes a back-up oscillator (540) which can be fabricated using conventional integrated circuit techniques, a clock monitor (520) for sensing a malfunction in a primary oscillator, and a multiplexer (530) for switching from the primary oscillator to the back-up oscillator (540) in the event the primary oscillator malfunctions. The back-up oscillator (540) is typically used in an anti-lock braking system to provide a safe shutdown after an oscillator malfunction. The internal back-up oscillator (540) being sufficient for execution of a shutdown routine.

IPC 1-7

**G06F 11/16**; G06F 1/04; B60T 8/88

IPC 8 full level

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