

Title (en)

PHASE LOCKED LOOP CONTROLLED FREQUENCY SYNTHESIZER FOR USE IN FREQUENCY HOPPING

Title (de)

DURCH EINEN PHASENREGELKREIS GESTEUERTER FREQUENZSYNTHESIZER ZUM GEBRAUCH IN FREQUENZSPRUNGBETRIEB

Title (fr)

SYNTHETISEUR DE FREQUENCES COMMANDE PAR UNE BOUCLE A PHASE ASSERVIE, DESTINE A ETRE UTILISE DANS LE SAUT DE FREQUENCE

Publication

**EP 0742974 A1 19961120 (EN)**

Application

**EP 95941673 A 19951205**

Priority

- EP 9504783 W 19951205
- GB 9424596 A 19941206

Abstract (en)

[origin: GB2295930A] A method for implementing frequency hopping in a time domain system having at least a first synthesiser 42 including the steps of locking onto a first frequency by the first synthesiser in wide loop bandwidth mode, transmitting from the first synthesiser, and converting to a narrow bandwidth mode on the first synthesiser after a delayed period of time. The apparatus includes two frequency synthesisers 42, 40 frequency hopping on a time slot by time slot basis. While one synthesiser e.g. 42 is in a narrow bandwidth and transmitting, the other synthesiser is disconnected and being tuned to the next frequency. Switch over between synthesisers and ramping up in power takes place in the guard band between the slots. Applications may be in TDMA based cellular systems. <IMAGE>

IPC 1-7

**H04B 1/713**; H03L 7/10

IPC 8 full level

**H03L 7/08** (2006.01); **H03L 7/107** (2006.01); **H03L 7/23** (2006.01); **H04B 1/713** (2011.01); **H04J 3/00** (2006.01)

CPC (source: EP US)

**H03L 7/1077** (2013.01 - EP US); **H03L 7/23** (2013.01 - EP US); **H04B 1/713** (2013.01 - EP)

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