

Title (en)

SEMICONDUCTOR STORAGE COMPONENT WITH A PLURALITY OF STORAGE CHIPS IN A SHARED CASING

Title (de)

HALBLEITER-SPEICHERBAUTEIL MIT MEHREREN SPEICHERCHIPS IN EINER GEMEINSAMEN UMHÜLLUNG

Title (fr)

COMPOSANT DE MEMORISATION A SEMI-CONDUSTEUR COMPORTANT PLUSIEURS PUCES MEMOIRES DANS UNE ENVELOPPE COMMUNE

Publication

**EP 0744084 A1 19961127 (DE)**

Application

**EP 95908875 A 19950206**

Priority

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Abstract (en)

[origin: WO9521459A1] A semiconductor storage component comprises a plurality of storage chips (C) of the same or different storage types (DRAM, SRAM, E(E)PROM, etc.) which are secured to a shared substrate (T) and connected to its outer terminals (A) via electrical connections (B), in which an outer terminal (A) of the substrate is precisely allocated to each contact point (K) or each storage chip (C). In a first embodiment, the substrate (T) fitted with several storage chips (C) has a casing (U) leaving its outer terminals (A) free and may be secured to a module plate (M). In a second embodiment, the substrate (T) with several storage chips (C) is secured to a module plate (M) and covered with a moulding (F) or a potting compound (H) to the side of the substrate (T) away from the module plate in such a way that the coating (U) on the part of the module plate (M) beneath the substrate (T) and the moulding (F) or potting compound (H) also comprises the outer terminals (A) of the substrate.

IPC 1-7

**H01L 23/495; H01L 23/31; H01L 21/56; G11C 5/00; H01L 25/065**

IPC 8 full level

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CPC (source: EP KR)

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