

Title (en)

METHOD AND APPARATUS FOR INTEGRATED CIRCUIT VOLTAGE REGULATION

Title (de)

VERFAHREN UND EINRICHTUNG FÜR INTEGRIERBARE SPANNUNGSREGELUNGSSCHALTUNGSANORDNUNG

Title (fr)

PROCEDE ET APPAREIL POUR LA REGULATION DE TENSION D'UN CIRCUIT INTEGRE

Publication

EP 0752167 A4 19980902 (EN)

Application

EP 95914116 A 19950317

Priority

- US 9503437 W 19950317
- US 21529394 A 19940321

Abstract (en)

[origin: WO9526064A1] On a printed circuit board (100), which contains a number of ICs (110) that require different voltage levels to operate, an IC package (400) that can be plugged into a standard IC socket (115) is utilized. IC package (400) contains voltage regulator (402) and capacitors (403) to convert the supply voltage at power plane (120) to the voltage level required by IC (401).

IPC 1-7

H02J 1/00

IPC 8 full level

G05F 1/56 (2006.01); **G06F 1/26** (2006.01); **G06F 13/40** (2006.01); **H05K 1/02** (2006.01); **H05K 1/18** (2006.01)

CPC (source: EP)

G05F 1/56 (2013.01); **G06F 1/26** (2013.01); **G06F 13/4063** (2013.01); **H05K 1/0262** (2013.01); **H05K 1/18** (2013.01); **H05K 2201/10015** (2013.01); **H05K 2201/10196** (2013.01); **H05K 2201/10689** (2013.01)

Citation (search report)

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- [PXY] PATENT ABSTRACTS OF JAPAN vol. 018, no. 670 (P - 1845) 16 December 1994 (1994-12-16)
- See references of WO 9526064A1

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