Title (en)
Method and apparatus for bit timing acquisition in a direct sequence spread spectrum receiver

Title (de)
Bittakterfassungsverfahren und -anordnung für Direktsequenzspreizspektrumempfänger

Title (fr)
Méthode et dispositif d'acquisition du signal binaire d'horloge dans un récepteur à spectre étalé par séquence directe

Publication
EP 0757458 A3 20000105 (EN)

Application
EP 96305590 A 19960730

Priority
US 50958895 A 19950731

Abstract (en)
[origin: EP0757458A2] In a direct sequence spread spectrum receiver, an apparatus for obtaining and adjusting bit synchronization. In one aspect, the bit synchronization is adjusted by selectively inverting a clocking circuit to delay sampling by one-half a clock cycle and to combine the inversion with a skipping of one cycle to advance the sampling by one-half cycle. In another aspect of the invention, the synchronization circuit avoids overflow of accumulating components by downshifting both the partial sums and the input data when needed. <IMAGE>

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H04L 7/033; H04J 13/00; H04L 7/04

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H03L 7/00 (2013.01 - KR); **H04B 1/70755** (2013.01 - EP US); **H04L 7/0054** (2013.01 - EP US); H04B 2201/70707 (2013.01 - EP US)

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