Title (en)

PROCESS FOR PREPARING FOR AND ACCOMPLISHING THE FUZZIFICATION OF A DIGITAL INPUT SIGNAL AT AN INPUT OF A FUZZY PROCESSOR

Title (de)

VERFAHREN ZUR VORBEREITUNG UND DURCHFÜHRUNG DER FUZZIFIZIERUNG EINES AN EINEM EINGANG EINES FUZZY-PROZESSORS ANLIEGENDEN DIGITALEN EINGANGSSIGNALES

Title (fr)

PROCEDE PERMETTANT DE PREPARER ET D'EFFECTUER LA MISE EN LOGIQUE FLOUE D'UN SIGNAL D'ENTREE NUMERIQUE SE TROUVANT A L'ENTREE D'UN PROCESSEUR A LOGIQUE FLOUE

Publication

EP 0757812 A1 19970212 (DE)

Application

EP 95915138 A 19950410

Priority

- DE 9500488 W 19950410
- DE 4415135 A 19940429

Abstract (en)

[origin: WO9530186A1] A fuzzy processor comprises a fuzzifier which calculates the appurtenance values of the relevant input-appurtenance functions from the digital input signal. To this end, the appurtenance functions must be stored. In order to reduce the storage space needed, especially at a high input signal resolution, it is of advantage to describe the appurtenance functions by shape-determined characteristics like restart points and slopes and store these. To save computing time, the definition range of the input signal is devided into equal segments. In the fuzzifiying process only the segment is considered in which the input signal to be fuzzified lies. This thus reduces the number of appurtenance functions to be considered, which reduces computing time. The result of the process is a fuzzification of the input signals using the minimum computing time without the need for excessively large storage space of the appurtenance functions.

IPC 1-7

G06F 7/60

IPC 8 full level

G06F 9/44 (2006.01); **G06N 7/02** (2006.01); **G06N 7/04** (2006.01)

CPC (source: EP)

G06N 7/04 (2013.01)

Citation (search report)

See references of WO 9530186A1

Designated contracting state (EPC)

DE FR GB IT

DOCDB simple family (publication)

WO 9530186 A1 19951109; EP 0757812 A1 19970212; JP H09512366 A 19971209

DOCDB simple family (application)

DE 9500488 W 19950410; EP 95915138 A 19950410; JP 52792995 A 19950410