

Title (en)
AN ITERATIVE METHOD OF RECORDING ANALOG SIGNALS

Title (de)
ITERATIVES VERFAHREN ZUM AUFZEICHNEN VON ANALOGEN SIGNALLEN

Title (fr)
PROCEDE ITERATIF D'ENREGISTREMENT DE SIGNAUX ANALOGIQUES

Publication
EP 0757836 A4 19990804 (EN)

Application
EP 95912592 A 19950223

Priority
US 9502258 W 19950223

Abstract (en)
[origin: WO9626523A1] Method and apparatus for adjustment and control of an iterative method of recording analog signals with on-chip trimming techniques for later playback. The invention allows setting of various parameters for the multi iterative programming technique after chip fabrication so as to allow tighter control and thus higher resolution analog signal sample storage in a given or minimum amount of time. Such parameters include, but are not limited to: the step down voltage [VSD] from the coarse programming cycle to the fine programming cycle, the incremental voltage increase between each fine pulse, the pulse width of each fine pulse, the number of fine pulses [Nf], the incremental voltage increase between each coarse pulse [VC], the pulse width of each coarse pulse, the number of coarse pulses [NC], and the offset [VOS], which stops further coarse pulses and holds the last coarse level as a reference for the following fine cycle.

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Citation (search report)

- No further relevant documents disclosed
- See references of WO 9626523A1

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