

Title (en)  
Improvements in display systems

Title (de)  
Verbesserungen bei Anzeigevorrichtungen

Title (fr)  
Améliorations dans les systèmes d'affichage

Publication  
**EP 0762737 A2 19970312 (EN)**

Application  
**EP 96113473 A 19960822**

Priority  
US 275795 P 19950824

Abstract (en)  
A processing system (10) for video data. The system uses two scan line video processors. The first processor (12) performs a first set of predetermined tasks. The second processor (20) performs a second set of predetermined tasks. The division of tasks between the two processors (12,20) allow optimal utilization of the processors' limited instruction space, any combination of processing tasks is allowed, so long as one of the processors works at near full instruction capacity. <IMAGE>

IPC 1-7  
**H04N 5/14**

IPC 8 full level  
**H04N 11/20** (2006.01); **G09G 3/34** (2006.01); **G09G 5/00** (2006.01); **H04N 5/14** (2006.01); **H04N 5/44** (2011.01); **H04N 5/66** (2006.01); **H04N 7/01** (2006.01); **G09G 3/20** (2006.01); **H04N 5/46** (2006.01); **H04N 5/74** (2006.01)

CPC (source: EP KR)  
**H04N 5/14** (2013.01 - EP); **H04N 5/74** (2013.01 - KR); **H04N 7/012** (2013.01 - EP); **G09G 3/20** (2013.01 - EP); **G09G 2340/0414** (2013.01 - EP); **H04N 5/46** (2013.01 - EP); **H04N 5/74** (2013.01 - EP)

Cited by  
CN109672841A; EP0992971A3; US6466269B1

Designated contracting state (EPC)  
DE FR GB IT NL

DOCDB simple family (publication)  
**EP 0762737 A2 19970312**; **EP 0762737 A3 19980513**; CA 2183796 A1 19970225; CN 1151655 A 19970611; JP H09187036 A 19970715; KR 970014285 A 19970329; SG 80561 A1 20010522; TW 341697 B 19981001

DOCDB simple family (application)  
**EP 96113473 A 19960822**; CA 2183796 A 19960821; CN 96113392 A 19960823; JP 22274296 A 19960823; KR 19960035350 A 19960824; SG 1996012138 A 19960824; TW 85111273 A 19960916