

Title (en)

TRANSITION DETECTION CIRCUIT

Title (de)

ÜBERGANGSDETEKTIONSSCHALTUNG

Title (fr)

CIRCUIT DE DETECTION DE TRANSITION

Publication

**EP 0764829 A1 19970326 (EN)**

Application

**EP 96905046 A 19960308**

Priority

- JP 9600585 W 19960308
- JP 9500675 W 19950406

Abstract (en)

A detector (1), with its object of detection having operation characteristics that include a shift from a first state to a second state, outputs a signal (e1) that corresponds to the operation characteristics of the object of detection. A first state detection circuit (2) generates a first output signal (SA) corresponding to the first state based upon the signal (e1) provided by the detector (1). A second state detection circuit (3) generates a second output signal (SB) corresponding to the second state based upon the signal provided by the detector (1). The second output signal (SB) is generated only when the object of detection shifts from the first state to the second state in a normal manner while the first output signal (SA) is being generated. A state judgement circuit (4) generates a state judgement output signal (Z) on condition that there has been a period of time during which the first output signal (SA) and the second output signal (SB) have been both at high. <IMAGE>

IPC 1-7

**G01D 1/10; G01P 13/00**

IPC 8 full level

**G01D 5/244** (2006.01); **G01D 5/245** (2006.01); **G01P 3/00** (2006.01); **G01P 3/486** (2006.01); **G01P 13/00** (2006.01); **G01R 31/00** (2006.01);  
**G08B 19/00** (2006.01)

CPC (source: EP US)

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Cited by

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