

Title (en)
DEVICE FOR DECODING DECODING ALGORITHMS AND METHOD OF ENCRYPTING AND DECODING SUCH ALGORITHMS USING THE DEVICE

Title (de)
ENTSCHLÜSSELUNGSEINRICHTUNG VON ENTSCHLÜSSELUNGsalgorithmen UND VERFAHREN ZUR DURCHFÜHRUNG DER VER- UND ENTSCHLÜSSELUNG DERSELBEN

Title (fr)
DISPOSITIF DE DECHIFFREMENT D'ALGORITHMES DE DECHIFFREMENT ET PROCEDE POUR LE CHIFFREMENT ET LE DECHIFFREMENT AU MOYEN D'UN TEL DISPOSITIF

Publication
EP 0765550 A1 19970402 (DE)

Application
EP 95920758 A 19950530

Priority
• DE 9500733 W 19950530
• DE 4420970 A 19940616

Abstract (en)
[origin: WO9534968A1] The aim of the invention is to develop a system and method which can work with variable encryption algorithms and which ensures the secure transfer of the algorithm without it being detected and prevents the algorithm without it being detected and prevents the algorithm being broken. This aim is achieved by virtue of the fact that the decoding device consists of an integrated circuit (1) associated with a central processing unit (2), an internal non-readable volatile random-access memory (3) used as working memory and an internal non-readable non-volatile read-only memory (4) plus an interface (5), each decoding device differing from every other by the content of the ROM (4) and being partly integrated in an integrated circuit (1) and that the interface (5) is located between the central processing unit (2) and the personal computer (6) and connected, together with central processing unit (2), to the personal computer (6) by a data path (a).

IPC 1-7
H04L 9/08

IPC 8 full level
H04L 9/08 (2006.01)

CPC (source: EP)
H04L 9/08 (2013.01)

Citation (search report)
See references of WO 9534968A1

Designated contracting state (EPC)
AT CH DE ES FR GB LI NL

DOCDB simple family (publication)
DE 4420970 A1 19951221; AU 3862795 A 19960105; EP 0765550 A1 19970402; WO 9534968 A1 19951221

DOCDB simple family (application)
DE 4420970 A 19940616; AU 3862795 A 19950530; DE 9500733 W 19950530; EP 95920758 A 19950530