Title (en)
PCM elementary switching matrix

Title (de)
PCM elementare Schaltmatrix

Title (fr)
Matrice de commutation élémentaire de signaux MIC

Publication
EP 0769887 A2 19970423 (EN)

Application
EP 96116279 A 19961011

Priority
IT MI952161 A 19951020

Abstract (en)
The invention relates to a PCM switching system employing switching elementary matrix controlled by a control microprocessor (mu P). Each elementary matrix including: one block for the control of incoming flows (BP_GFE) suitable to receive flows whose bit-rate is preferably separately programmable; one programming and control block for outgoing flows (BP_GFU) suitable to make available at output those flows whose bit-rate is preferably separately programmable; one connection control block (BCC) suitable to ciclically and constantly control the correctness of the connection operations; one timing receipt and distribution block (BR_DT), one synchronism phase shifting detection block (BRSS), one microprocessor interface block (BIM). <IMAGE>

IPC 1-7
H04Q 11/06

IPC 8 full level
H04Q 11/06 (2006.01)

CPC (source: EP)
H04Q 11/06 (2013.01)

Citation (applicant)
EP 0039948 A1 19811118 - CSELT CENTRO STUDI LAB TELECOM [IT]

Cited by
US6442160B1; WO9912384A3

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DE FR NL SE

DOCDB simple family (publication)
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