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Application
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- US 70077091 A 19910515

Abstract (en)
[origin: WO9215948A1] In a situation where a first computer program has been translated to obtain a second computer program, an error occurring during execution of the second computer program is reported in the context of the first program. This is done by aborting execution of the second computer program when the error occurs; determining a first address which is the address of the instruction in the second computer program that caused the error; determining from the first address a second address of an instruction in the first computer program from which the instruction in the second computer program was translated; and reporting that the error occurred, and using the second address to indicate that the error is associated with the instruction in the first computer program. Preferably the second address is used to reference traceback and symbolic name information generated when the first computer program is compiled from source code. The traceback information provides the line number of the source code from which the instruction in the first computer program was compiled, and the symbolic name information provides the name of a routine containing the instruction in the first program or a variable used by the instruction.

IPC 1-7
G06F 9/38; G06F 9/318

IPC 8 full level
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CPC (source: EP US)
G06F 8/52 (2013.01 - EP US); **G06F 9/3861** (2013.01 - EP US); **G06F 11/3624** (2013.01 - EP US)

Citation (search report)
- [A] EP 0211384 A2 19870225 - WANG LABORATORIES [US]
- [A] WO 8807718 A1 19881006 - INSIGNIA SOLUTIONS LIMITED [GB]
- [A] ROBIN W. EDENFIELD ET AL.: "The 68040 Processor / Part 1, Design and Implementation", IEEE MICRO, vol. 10, no. 1, February 1990 (1990-02-01), NEW YORK US, pages 66 - 78, XP000102440

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