

Title (en)  
BLOCK FAULT TOLERANCE IN INTEGRATED PRINTING HEADS

Title (de)  
BLOCKFEHLERTOLERANZ IN INTEGRIERTEN DRUCKKÖPFEN

Title (fr)  
TOLERANCE AUX DEFAUTS DE BLOCS DANS LES TETES D'IMPRESSION INTEGREES

Publication  
**EP 0772524 A1 19970514 (EN)**

Application  
**EP 96911670 A 19960410**

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Abstract (en)  
[origin: WO9632264A1] Single faults in shift registers incorporated on monolithic printing heads can render inoperable large numbers of printing actuators, as data will either be stuck high or stuck low for subsequent shift register and actuator stages. This can reduce the effectiveness of other means of fault tolerance, and increase the device sensitivity to faults in individual, normally redundant, actuators. A printing head is disclosed which provides block fault tolerance in the shift registers, limiting the effect of shift register fabrication faults to small numbers of redundant actuators. This allows a high probability of defect correction by other forms of fault tolerance integrated on the chip, thereby increasing overall device yield.

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IPC 8 full level  
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Citation (search report)  
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