

Title (en)

Comparator with hysteresis for use in a voltage regulating circuit

Title (de)

Hysteresesebehaftete Komparatorschaltung zur Verwendung bei einer Spannungsregelungsschaltung

Title (fr)

Comparteur à hystérésis pour utilisation dans un circuit régulateur de tension

Publication

EP 0774705 B1 20030723 (DE)

Application

EP 96118126 A 19961112

Priority

DE 19542823 A 19951116

Abstract (en)

[origin: EP0774705A2] The comparator circuit has a differential stage (D) forming a component of a cascade circuit (L,D,S,G) which has on one side of the differential stage a load stage (L) with load transistors (MN1,MP2) and on the other side a negative feed back stage (G). A reference voltage is applied both to the control electrode of a first load transistor (MN1), which has a high impedance input and supplies the voltage to be compared, and to the control electrode of a second load transistor (MP2). The first transistor represents a constant load impedance and a third load transistor (MP2) is connected in parallel with the second transistor which is switched on or off depending on signals from the comparator circuit. The result is that a further load impedance is either connected or not connected in parallel with the second transistor.

IPC 1-7

H03K 3/0233; H03K 3/3565; G05F 3/26

IPC 8 full level

G05F 3/26 (2006.01)

CPC (source: EP US)

G05F 3/267 (2013.01 - EP US)

Citation (examination)

- US 3938055 A 19760210 - BUHLER FREDERICK T
- PATENT ABSTRACTS OF JAPAN vol. 008, no. 049 (E - 230) 6 March 1984 (1984-03-06)

Designated contracting state (EPC)

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EP 0774705 A2 19970521; **EP 0774705 A3 19980128**; **EP 0774705 B1 20030723**; DE 19542823 A1 19970522; DE 19542823 C2 19970904; US 5739705 A 19980414

DOCDB simple family (application)

EP 96118126 A 19961112; DE 19542823 A 19951116; US 74676896 A 19961115