

Publication

EP 0775993 A3 19970611

Application

EP 96118196 A 19961113

Priority

JP 29957695 A 19951117

Abstract (en)

[origin: EP0775993A2] A DC plasma display panel has a row-column matrix of display cells and auxiliary cells, and a row of reset cells adjacent to the first row of auxiliary cells. The display cells and auxiliary cells are driven a row at a time in a sequence that starts from the first row and repeats cyclically. The reset cells are driven during a part of this sequence in which display and auxiliary cells in rows other than the first row are driven, preferably including the time during which the display and auxiliary cells in the last row are driven. <IMAGE>

IPC 1-7

G09G 3/28

IPC 8 full level

G09G 3/282 (2006.01); **G09G 3/20** (2006.01); **G09G 3/28** (2013.01); **G09G 3/291** (2013.01)

CPC (source: EP)

G09G 3/2022 (2013.01); **G09G 3/282** (2013.01)

Citation (search report)

- [A] US 5150011 A 19920922 - FUJIEDA YOSHIHIRO [JP]
- [PX] PATENT ABSTRACTS OF JAPAN vol. 97, no. 1 31 January 1997 (1997-01-31)

Designated contracting state (EPC)

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EP 0775993 A2 19970528; **EP 0775993 A3 19970611**; JP H09146491 A 19970606; TW 312784 B 19970811

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