

Title (en)

Signal modification circuit

Title (de)

Signalmodifikationsschaltung

Title (fr)

Circuit pour la modification d'un signal

Publication

EP 0776144 B1 20010411 (DE)

Application

EP 95118595 A 19951125

Priority

EP 95118595 A 19951125

Abstract (en)

[origin: EP0776144A1] The circuit is used for a pair of signals (L,R) provided by a signal source (q), which are divided into signal components (s1-s6), and selectively combined to provide a pair of modified signals (L',R'). A first combining stage (K1) combines two signal components (s1,s2) obtained from the first signal (L), provided by the source, with a third signal (s3) obtained from the second signal, provided by the source. A second combining stage (K2) similarly combines two signal components (s4,s5), obtained from the second signal, with a signal (s6) obtained from the first signal.

IPC 1-7

H04S 1/00; H04S 3/00

IPC 8 full level

H04S 1/00 (2006.01)

CPC (source: EP KR US)

H04S 1/00 (2013.01 - KR); **H04S 1/002** (2013.01 - EP US)

Designated contracting state (EPC)

DE FR GB IT NL

DOCDB simple family (publication)

EP 0776144 A1 19970528; EP 0776144 B1 20010411; DE 59509187 D1 20010517; JP H09191499 A 19970722; KR 100424520 B1 20040618; KR 970032266 A 19970626; US 5822437 A 19981013

DOCDB simple family (application)

EP 95118595 A 19951125; DE 59509187 T 19951125; JP 31183696 A 19961122; KR 19960056382 A 19961122; US 75414496 A 19961122