

Title (en)
Digital demodulator

Title (de)
Digitaler Demodulator

Title (fr)
Démodulateur numérique

Publication
EP 0786921 B1 20020717 (DE)

Application
EP 96101105 A 19960126

Priority
EP 96101105 A 19960126

Abstract (en)
[origin: EP0786921A1] From the digitised phase (I) and quadrature (Q) outputs of a quadrature signal source (2), a co-ordinate convertor (3) forms an amplitude signal (b) and a first phase signal (p1). Two control loops (4,9) zero the slope of the first phase signal and adjust its time-averaged value to the zero-phase position. The process produces a second and a third phase signal (p2,p3) of which the latter is used in conjunction with its tangent (p3'), and the amplitude signal in a decoder (12). f From these signals the left and right-hand channel signals (L,R), and the pilot signal (P), are obtained.

IPC 1-7
H04S 1/00

IPC 8 full level
H04S 1/00 (2006.01)

CPC (source: EP US)
H04S 1/007 (2013.01 - EP US)

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EP 0786921 A1 19970730; EP 0786921 B1 20020717; DE 59609450 D1 20020822; US 5767739 A 19980616

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EP 96101105 A 19960126; DE 59609450 T 19960126; US 79292497 A 19970121