

Title (en)

Device for current reference in an integrated circuit

Title (de)

Vorrichtung zur Erzeugung von Referenzstrom in einer integrierten Schaltung

Title (fr)

Dispositif de référence de courant en circuit intégré

Publication

EP 0788047 B1 19981007 (FR)

Application

EP 97400209 A 19970129

Priority

- FR 9601168 A 19960131
- FR 9607705 A 19960620

Abstract (en)

[origin: EP0788047A1] The current reference device has a reference resistor (Rr) and two MOS transistors of the same conductivity type. The first transistor (T1) has gate and drain connected together at one terminal (A) of the reference resistor, and the second transistor (T2) has gate and drain connected together at the other terminal (B) of the reference resistor. The first transistor has a threshold voltage greater than the second transistor and the source of each of the transistors is connected to the same potential as the circuit substrate. A third transistor (T3), also with gate and drain connected, has a higher threshold potential than the first transistor, and is connected to it to provide a current proportional to the difference in threshold potentials.

IPC 1-7

G05F 3/26

IPC 8 full level

G05F 3/26 (2006.01)

CPC (source: EP US)

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Cited by

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Designated contracting state (EPC)

DE FR GB IT

DOCDB simple family (publication)

EP 0788047 A1 19970806; EP 0788047 B1 19981007; DE 69700031 D1 19981112; DE 69700031 T2 19990225; FR 2744263 A1 19970801; FR 2744263 B3 19980327; US 5903141 A 19990511

DOCDB simple family (application)

EP 97400209 A 19970129; DE 69700031 T 19970129; FR 9607705 A 19960620; US 79138397 A 19970130