

Title (en)  
Regulated power supply

Title (de)  
Geregelte Versorgungsspannungsquelle

Title (fr)  
Source d'alimentation régulée

Publication  
**EP 0795808 B1 19991222 (DE)**

Application  
**EP 97200692 A 19970307**

Priority  
DE 19609971 A 19960314

Abstract (en)  
[origin: EP0795808A1] The parallel regulator has a controllable semiconductor in the form of a Darlington pair of n-p-n transistors (1,2) with emitters interconnected by a resistance (3). The emitter-collector paths interconnect the two output terminals (4,5). The reference input (25) drives the base of an emitter-follower (27) in cascode with a constant-current source (14-16) feeding a regulating amplifier of the actual-value input (30). A bias current is supplied in accordance with the difference between the actual-value and reference voltages.

IPC 1-7  
**G05F 1/613**

IPC 8 full level  
**G05F 1/613** (2006.01); **G05F 3/26** (2006.01); **H03F 3/45** (2006.01)

CPC (source: EP US)  
**G05F 3/265** (2013.01 - EP US)

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**EP 97200692 A 19970307**; DE 19609971 A 19960314; DE 59700869 T 19970307; JP 5918897 A 19970313; US 81813997 A 19970313