

Title (en)

Circuit arrangement for picture-in-picture insertion

Title (de)

Schaltungsanordnung zur Bild-im-Bild-Einblendung

Title (fr)

Circuit d'insertion d'image dans l'image

Publication

EP 0798690 B1 20080910 (DE)

Application

EP 97104980 A 19970324

Priority

DE 19611693 A 19960325

Abstract (en)

[origin: EP0798690A2] The circuit blends at least one small image (22-4) into a large image (21) which is divided into rows. The circuit contains a first memory, in which are stored the contents of the small images. A second memory contains an overview contg. data values for an image region (a-g) contg. a number of successive lines yielding changes occurring in the line direction w.r.t. the small image to be represented. The overview contains start and end points for the small image and a pointer to the image contents in the first memory. A controller controls the reading of the image contents (x,y or z) from the first memory according to the data values which are stored in the overview.

IPC 8 full level

G09G 1/16 (2006.01); **G09G 5/14** (2006.01)

CPC (source: EP)

G09G 5/14 (2013.01); **G09G 2340/125** (2013.01)

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