

Title (en)  
Integrated circuit current source

Title (de)  
Integrierte Stromquellenschaltung

Title (fr)  
Source de courant en circuit intégré

Publication  
**EP 0800131 B1 19981202 (FR)**

Application  
**EP 97400761 A 19970402**

Priority  
FR 9604262 A 19960404

Abstract (en)  
[origin: EP0800131A1] The circuit is based on a p-channel MOS transistor (2) biased to saturation by a Zener diode (DZ) whose breakdown voltage (VZ) is not less than the supply voltage (Vcc) applied via a charge pump (PC). This transistor delivers a reference current (Iref), which is substantially independent of temperature, to an n-channel MOS transistor (3) connected as a diode. The reference current is easily divided by 100 or 1000 in two successive current mirrors using transistors (5,4;6,8) of very reasonable size. The capacitor (C) may be charged under the control of a circuit (7) ensuring the desired durations of step and ramp voltage.

IPC 1-7  
**G05F 3/18**

IPC 8 full level  
**G05F 3/18** (2006.01)

CPC (source: EP)  
**G05F 3/185** (2013.01)

Cited by  
EP2722727A1; FR2994750A1; US9772647B2

Designated contracting state (EPC)  
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DOCDB simple family (publication)  
**EP 0800131 A1 19971008; EP 0800131 B1 19981202**; DE 69700060 D1 19990114; DE 69700060 T2 19990429; FR 2747249 A1 19971010; FR 2747249 B1 19980522

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