

Title (en)  
Noise-damping arrangement for damping noise currents of electronic devices

Title (de)  
Dämpfungseinrichtung gegen Störströme elektronischer Komponenten

Title (fr)  
Dispositif d'atténuation des courants parasites de composants électroniques

Publication  
**EP 0801443 B1 20030219 (DE)**

Application  
**EP 97105377 A 19970401**

Priority  
DE 19614507 A 19960412

Abstract (en)  
[origin: EP0801443A1] The HF interference current suppression device uses an inductance through which the operating current is passed, provided by an annular core (7) of an amorphous or nano-crystalline material with a relative permeability of above 20000. The core is fitted over a pin (4) within a plug connector housing (1). A capacitor can be connected between the operating current lead and earth, with the annular core insulated relative to the pin and connected to earth, so that it acts as a capacitor electrode.

IPC 1-7  
**H01R 13/719**

IPC 8 full level  
**H01F 1/16** (2006.01); **H01F 17/06** (2006.01); **H01F 27/00** (2006.01); **H01G 4/40** (2006.01); **H01R 13/719** (2011.01)

CPC (source: EP)  
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**EP 97105377 A 19970401**; DE 59709325 T 19970401; JP 10388997 A 19970407