

Title (en)

System and method for optimizing storage requirements for an N-way distribution channel

Title (de)

System und Verfahren zum Optimierung des Speicherbedarfs für einen N-teiligen Verteilungskanal

Title (fr)

Système et méthode optimisant les exigences de mémorisation pour un canal de distribution à N voies

Publication

EP 0803859 A3 19980304 (EN)

Application

EP 96118476 A 19961118

Priority

US 63625096 A 19960423

Abstract (en)

[origin: EP0803859A2] In a texture mapping computer graphics system including a texture mapping chip (46) that stores a plurality of texels (S, T) and multiple frame buffer controller chips (50A-50E) that process the texels, an interface is provided between the texture mapping chip (46) and the frame buffer controller chip. The interface includes a texel array storage unit, coupled between the texture mapping chip (46) and the frame buffer controller chips (50A-50E), that temporarily stores a limited number of texels, each texel being destined for a particular frame buffer controller chip. A control unit (110), coupled to the texel array storage unit (90), controls shifting texels from the texture mapping chip (46) into locations within the texel array storage unit (90) and transferring texels from the texel array storage unit (90) into appropriate frame buffer controller chips (50A-50E). A plurality of address storage units (114A-114E), coupled to the control unit (90), store addresses of locations within the texel array storage unit (90) in which texels are stored. Each address storage unit (114A-114E) corresponds to a different frame buffer controller chip (50A-50E). <IMAGE>

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G09G 5/36

IPC 8 full level

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CPC (source: EP)

G09G 5/393 (2013.01); **G09G 2360/121** (2013.01); **G09G 2360/123** (2013.01)

Citation (search report)

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Designated contracting state (EPC)

DE FR GB

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